## Electrical Impedance Measurements:

## Algorithms for impedance estimation and equivalent circuit parameters determination



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## Introduction

- What is electrical impedance?
- Why does it matter?
- How do we measure electrical impedance?
- How do we know the circuit of the measured impedance?


## What is electrical impedance?

- In DC (direct current), the relation between the voltage and the current applied to a conductor, is the electrical resistance.
- This is Ohm's law.


$$
\begin{aligned}
& I=\frac{U}{R} \\
& R=\frac{U}{I}
\end{aligned}
$$

## What is electrical impedance?

- What happens when the voltage is a sinewave AC (from alternate current)?


$$
R=\frac{U_{R M S}}{I_{R M S}} \quad U_{R M S}=\sqrt{\frac{1}{T} \int_{T} u^{2}(t) d t}
$$



## What is electrical impedance?

- What if the resistor is replaced with a capacitor?


$$
\frac{u(t)}{i(t)}=\text { ? }
$$



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## What is electrical impedance?

- With sinewaves, it is better not to use $u(t)$ and $i(t)$ and use instead phasors.
- These are complex numbers that fully describe the sinewave

$$
\begin{aligned}
& u(t)=U \cos \left(2 \pi f t+\varphi_{U}\right) \quad \longleftrightarrow \bar{U}=U e^{j \varphi_{U}} \\
& i(t)=I \cos \left(2 \pi f t+\varphi_{I}\right) \quad \longleftrightarrow \bar{I}=I e^{j \varphi_{I}}
\end{aligned}
$$

- The frequency $f$ is implicit when phasors are used ( $\omega=2 \pi f$ ).
- $f$ is needed to go back from phasors to $u(t)$ and $i(t)$.
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## What is electrical impedance?

- Back to the capacitor circuit. How/where are the phasors?



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## What is electrical impedance?

- What if the load is now an inductor?


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## What is electrical impedance?

- Electrical impedance can be defined with sinewave stimulus using the phasors

$$
\begin{aligned}
& \bar{U}=U e^{j \varphi_{U}} \\
& \bar{I}=I e^{j \varphi_{I}}
\end{aligned}
$$

- The electrical impedance $Z$ is

$$
Z=\frac{\bar{U}}{\bar{I}}=\frac{U}{I} e^{j\left(\varphi_{U}-\varphi_{I}\right)}=|Z| e^{j \varphi_{Z}}
$$



## What is electrical impedance?

- These basic elements can be combined. For example:



## Why does it matter?

- Some examples:
- Electrical impedance is used in many sensors where the measurand changes the impedance frequency response of the sensor.
- Bioimpedance is used in non-invasive test monitoring of living organisms.
- In rechargeable battery systems, the battery output impedance can be used to estimate the battery SOH (state of health).


## Why does it matter?

- To derive, useful information from the impedance, it must be measured at different frequencies. Example, for a vibrating wire viscosity sensor:

Impedance frequency response


Nyquist Plot


- Impedance Spectroscopy (IS)


## How is electrical impedance measured?

- Pre-digital, impedance measurement methods included mostly bridges:

- Manual adjustment of $Z_{1}, Z_{2}$ and $Z_{3}$ to cancel $i$, leads to $Z_{\mathrm{x}}=Z_{1} Z_{3} / Z_{2}$.
- High level of expertise and experience required.
- Measuring many, many frequencies is almost impossible.


## How is electrical impedance measured?

- Evolution of analog electronics, ADCs and digital signal processors, have led to the development of highly efficient impedance measurement devices.

Auto-balancing bridge method


Voltage-current method


## How is electrical impedance measured?

- In these two measurement setups, ADCs acquire samples from two sinewave voltages.
- Algorithms estimate the sinewave parameters and from there, the impedance parameters ( $|Z|$ and $\varphi_{Z}$ ) are estimated.
- The efficiency of these algorithms is crucial. This includes their uncertainty and also how long does the calculation require.
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## How is electrical impedance measured?

- One algorithm that we have extensively used is sine-fitting.
- It estimates the sinewave parameters (amplitude, phase, DC component and frequency) of a set of acquired samples from a sinewave voltage.




## How is electrical impedance measured?

- Measurement setup


$$
\left|Z_{X}\right|=\frac{D_{2}}{D_{1}}\left|Z_{R}\right| \quad \varphi_{Z}=\Delta \varphi+\varphi_{Z_{R}}
$$

## How is electrical impedance measured?

- Implemented in a dsPIC based embedded measurement system.



## How is electrical impedance measured?

- How to measure the impedance frequency response?
- Method A: Single-tone sweep method

1. Set the measurement frequency on the generator;
2. Acquire samples from both ADCs;
3. Use sine-fitting to estimate sinewave voltage parameters and from them, the impedance parameters (for that measurement frequency);
4. Repeat steps 1-3 for the next measurement frequencies.

## How is electrical impedance measured?

- How to measure the impedance frequency response?
- Method B: Multi-harmonic method

1. Design multi-harmonic stimulus;
2. Upload stimulus to arbitrary waveform generator (AWG);
3. Acquire samples from both ADCs;
4. Use multi-harmonic waveform fitting algorithm to estimate harmonic amplitude and phases of the acquired voltages and from them, the impedance parameters (at the frequencies of all harmonics).
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## How is electrical impedance measured?

- Example 1: $f=1 \mathrm{kHz}$, with higher fundamental and 48 lower amplitude harmonics


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## How is electrical impedance measured?

- Results from example 1 with RLC parallel circuit with resonance near 9 kHz .




## How is electrical impedance measured?

- Example 2: $f=100 \mathrm{~Hz}$, without fundamental and 250 harmonics in the 10 kHz to 25 kHz range. Impedance is an LC parallel circuit.



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## How do we know the impedance circuit?

- This is split into two parts:
- Part 1: If the circuit topology is known, how do we estimate the circuit parameters?
- Part 2: If the circuit topology is unknown, how do we determine the circuit topology?
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How to estimate the circuit parameters?

- Option 1: CNLS (Complex Nonlinear Least-Squares)
- Developed by J. R. Macdonald.
- It is a algorithm that, given a circuit topology, estimates the values of the components that best fit with the measured IFR.
- It is a closed program with multiple circuit topologies.
- Highly configurable but requires good initial estimates to converge to the correct circuit parameters. telecomunicações


## How to estimate the circuit parameters?

- Option 2: Genetic Algorithms (GAs)
- Genetic algorithms can efficiently search a vast, multidimensional search space and find the approximate location of the absolute minimum.
- To effectively search the multi-decade search space (associated with component values), the estimated value is the log 10 base value of the component value.
- Requires many iterations and fine-tuning.
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## How to identify the impedance circuit?

- Option 1: Vector Fitting (VF)

Estimates the coefficients of two polynomials whose ratio fits the measured Impedance Frequency Response (IFR).

It does not estimate directly the circuit topology.

- Option 2: Gene Expression Programming (GEP)

Evolutionary algorithm specifically used to obtain the circuit topology that best fits the measured IFR.

## How to identify the impedance circuit?

- Gene Expression Programming (GEP)

The circuit topology is described in a tree structure.
Example:

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## How to identify the impedance circuit?

- Gene Expression Programming (GEP)
- A set of circuits (population) is evaluated to assess its fitness.
- Each circuit is described by a Gene which is a sequence of elements.
- There is a maximum sequence size.
- To ensure that the sequence corresponds to a valid circuit, it has a head and a tail. The head has operators and components while the tail only has components.
- GEP operations combine and change these sequences.


## How to identify the impedance circuit?

- Gene Expression Programming (GEP) Operations
- Replication: a new population of circuits is obtained from the previous one based on their fitness.
- Mutation: some random positions on a few random genes are changed.
- Transposition: parts of the gene are copied to another location within the gene.
- Recombination: pairs of randomly chosen genes exchange part of their gene code.
- In mutation and transposition, care must be taken to ensure that GEP coding rules are maintained.
- The best gene is always carried to next generation (survival of the fittest).


## How to identify the impedance circuit?

- Gene Expression Programming (GEP)



## How to identify the impedance circuit?

- Gene Expression Programming (GEP)
- Simplification routine identifies components that can be removed from the circuit:

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## How to identify the impedance circuit?

- Gene Expression Programming (GEP)
- Using more complex circuit elements:

| Series Combinations |  | Parallel Combinations |  |
| :---: | :---: | :---: | :---: |
| Code | Type | Code | Type |
| 12 | $\mathrm{R}+\mathrm{L}$ | -12 | $\mathrm{R} / / \mathrm{L}$ |
| 13 | $\mathrm{R}+\mathrm{C}$ | -13 | $\mathrm{R} / / \mathrm{C}$ |
| 23 | $\mathrm{~L}+\mathrm{C}$ | -23 | $\mathrm{~L} / / \mathrm{C}$ |
| 123 | $\mathrm{R}+\mathrm{L}+\mathrm{C}$ | -123 | $\mathrm{R} / / \mathrm{L} / \mathrm{C}$ |



## How to identify the impedance circuit?

- Gene Expression Programming (GEP)
- In most cases, with convergence, additional components are added to the gene.
- These do not affect the impedance response at the measured frequency.
- However, they appear as longer genes.
- And it's not the correct circuit!


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## How to identify the impedance circuit?

- Gene Expression Programming (GEP)
- How to remove components that are not needed?



## How to identify the impedance circuit?

- Gene Expression Programming (GEP)
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## How to identify the impedance circuit?

- Gene Expression Programming (GEP)
- How to remove components that are not needed?



## How to identify the impedance circuit?

- Gene Expression Programming (GEP)
- Has this process helped?
- Average gene size $5.7 \rightarrow 4.3$.
- Average number of iterations $6.9 \rightarrow 3.8$.

- Convergence to correct circuit 15.6 \% $\rightarrow 56.8$ \%.
- Convergence $96 \% \rightarrow 100 \%$.



## How to identify the impedance circuit?

- Gene Expression Programming (GEP) MEASUREMENT results



## How to identify the impedance circuit?

- Gene Expression Programming (GEP) MEASUREMENT results




Experimental standard deviation:
R: 0.11 \% $\mathrm{C}_{1}: 0.06$ \% L: 0.06 \% $\mathrm{C}_{2}: 0.05 \%$

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## How to identify the impedance circuit?

- Vector Fitting (VF)
- The VF algorithm estimates the coefficients of two polynomials whose ratio fits the measured IFR.
- The algorithm does not estimate the circuit topology.
- However, from the polynomials an iterative procedure to retrieve the circuit topology has been developed.
- This is still a work in progress....


## Examples of embedded impedance measurement systems

- A Low Cost Miniaturized Impedance Analyzer (2010)
- Based on two AD5933, 1 MHz , 12-bit impedance converter, network analyzer from Analog Devices.
- Impedance range: $10 \Omega$ up to $10 \mathrm{G} \Omega$.
- Frequencies: 0.01 Hz up to 100 kHz .
- Maximum errors of $2 \%$ and $2.5^{\circ}$.

J. Hoja and G. Lentka,
"Interface circuit for impedance sensors using two specialized single-chip microsystems", Sensors and Actuators A: Physical, vol. 163, pp. 191-197, 2010. doi: 10.1016/j.sna.2010.08.002

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## Examples of embedded impedance measurement systems

- Seven-Decade Handheld-Impedance-Measurement System (2015)
- Specifically designed for electrochemical measurements.
- Processor MSP430 with 12-bit, 200 kS/s.
- Impedance range: $100 \Omega$ up to $100 \mathrm{G} \Omega$.
- Frequencies: 0.01 Hz up to 100 kHz .
- Uncertainty of $5 \%$ and $3^{\circ}$.
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## Examples of embedded impedance measurement systems

- Embedded System for Viscosity Measurements (2015)
- Based on a Analog Devices DSP with two external 16-bit ADCs with $1 \mathrm{MS} / \mathrm{s}$.
- Stimulus from a 14-bit DAC.
- Frequencies: 100 Hz up to 10 kHz .
- Multi-harmonic stimulus with multi-harmonic fit.
- 4-wire connection to impedance.
- Sensor has very low impedance (near $1 \Omega$ ).

J. Santos, F. Janeiro and P. M. Ramos,

Development, implementation, and characterization of a standalone embedded viscosity measurement system based on the impedance spectroscopy of a vibrating wire sensor", Measurement Science and Technology, vol. 26, n. ${ }^{\circ} 10$, pp. 105903-1-14, Oct. 2015
doi: 10.1088/0957-0233/26/10/105903

## Conclusions

- Very old topic!
- Major recent developments within the field due to evolution of analog electronics, ADCs and portable low cost digital signal processing.
- Application in many fields.
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## The end

- Thank you for your attention


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